

4.9-5.8 GHz High-Linearity Power Amplifier

SST11LP12



Preliminary Specifications

FEATURES:

- **High Gain:**
 - Typically 35 dB gain across 4.9-5.8 GHz over temperature 0°C to +85°C
- **High linear output power:**
 - ~28 dBm P1dB (Pulsed single-tone signal)
 - Meet 802.11a OFDM ACPR requirement up to 23+ dBm over ~ entire band
 - Added EVM~4% up to 21 dBm for 54 Mbps 802.11a signal
- **High power-added efficiency/Low operating current for 54 Mbps 802.11a applications**
 - ~12% @ $P_{OUT} = 21$ dBm for 54 Mbps
- **Built-in Ultra-low I_{REF} power-up/down control**
 - $I_{REF} < 3$ mA
- **Low idle current**
 - ~130 mA I_{CQ}
- **High speed power up/down**
 - Turn on/off time (10%~90%) <100 ns
 - Typical power-up/down delay with driver delay included <200 ns
- **High temperature stability**
 - ~1.5/1.0 dB gain/power variation between 0°C to +85°C
 - ~1 dB detector variation over 0°C to +85°C
- **Low shut-down current (< 0.1 μ A)**
- **On-chip power detection**
- **20 dB dynamic range on-chip power detection**
- **Simple input/output matching**
- **Packages available**
 - 16-contact VQFN (3mm x 3mm)
 - Non-Pb (lead-free) packages available

APPLICATIONS:

- **WLAN (IEEE 802.11a)**
- **Japan WLAN**
- **HyperLAN2**
- **Multimedia**

PRODUCT DESCRIPTION

The SST11LP12 is a high-power, high-gain power amplifier based on the highly-reliable InGaP/GaAs HBT technology.

The SST11LP12 can be easily configured for high-power, high-efficiency applications with superb power-added efficiency while operating over the entire 802.11a frequency band for U.S., European, and Japanese markets (4.9-5.8 GHz). It typically provides 35 dB gain with 16% power-added efficiency @ $P_{OUT} = 23$ dBm.

The SST11LP12 has excellent linearity, typically ~4% added EVM at 21 dBm output power which is essential for 54 Mbps 802.11g operation while meeting 802.11g spectrum mask at 23+ dBm. SST11LP12 also has wide-range (>20 dB), temperature-stable (~1 dB over 85°C), single-ended/differential power detectors which lower users' cost on power control.

The power amplifier IC also features easy board-level usage along with high-speed power-up/down control. Ultra-low reference current (total $I_{REF} < 3$ mA) makes the SST11LP12 controllable by an on/off switching signal directly from the baseband chip. These features coupled with low operating current make the SST11LP12 ideal for the final stage power amplification in battery-powered 802.11a WLAN transmitter and access point applications.

The SST11LP12 is offered in 16-contact VQFN package. See Figure 1 for pin assignments and Table 1 for pin descriptions.

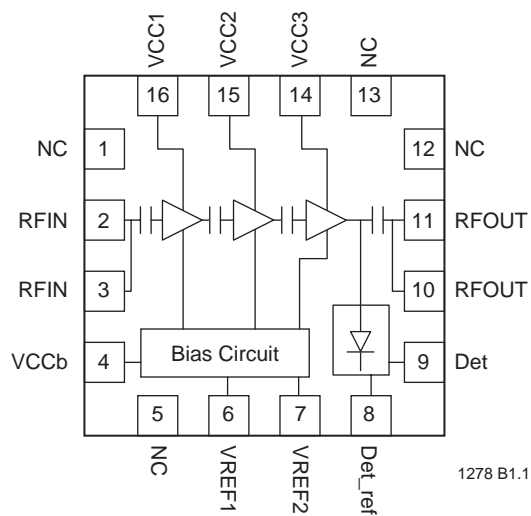


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FUNCTIONAL BLOCKS

FUNCTIONAL BLOCK DIAGRAM





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PIN ASSIGNMENTS

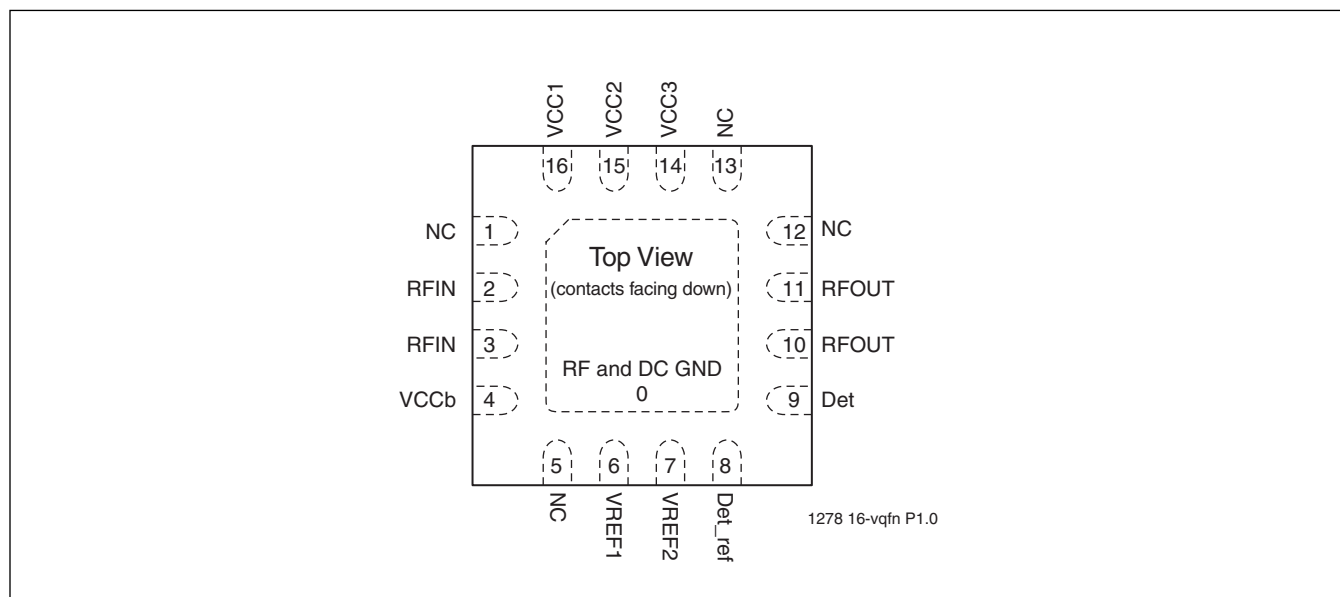


FIGURE 1: PIN ASSIGNMENTS FOR 16-CONTACT VQFN

PIN DESCRIPTIONS

TABLE 1: PIN DESCRIPTION

Symbol	Pin No.	Pin Name	Type ¹	Function
GND	0	Ground		The center pad should be connected to RF ground with several low inductance, low resistance vias.
NC	1	No Connection		Unconnected pins
RFIN	2		I	RF input, DC decoupled
RFIN	3		I	RF input, DC decoupled
VCCb	4	Power Supply	PWR	Supply voltage for bias circuit
NC	5	No Connection		Unconnected pins.
VREF1	6		PWR	1st and 2nd stage current control
VREF2	7		PWR	3rd stage current control
Det_ref	8		O	On-chip power detector reference
Det	9		O	On-chip power detector
RFOUT	10		O	RF output
RFOUT	11		O	RF output
NC	12	No Connection		Unconnected pins.
NC	13	No Connection		Unconnected pins.
VCC3	14	Power Supply	PWR	Power supply, 3rd stage
VCC2	15	Power Supply	PWR	Power supply, 2nd stage
VCC1	16	Power Supply	PWR	Power supply, 1st stage

1. I=Input, O=Output

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ELECTRICAL SPECIFICATIONS

The AC and DC specifications for the power amplifier interface signals. Refer to Table 2 for the DC voltage and current specifications. Refer to Figures 2 through 10 for the RF performance.

Absolute Maximum Stress Ratings (Applied conditions greater than those listed under "Absolute Maximum Stress Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these conditions or conditions greater than those defined in the operational sections of this data sheet is not implied. Exposure to absolute maximum stress rating conditions may affect device reliability.)

Supply Voltage at pins 4, 14, 15, 16 (V_{CC}) -0.3V to +5.5V
 DC supply current (I_{CC}) 500 mA
 Operating Temperature (T_A) -40°C to +85°C
 Storage Temperature (T_{STG}) -40°C to +120°C
 Maximum Junction Temperature (T_J) +150°C
 Surface Mount Solder Reflow Temperature: "with-Pb" units¹: 240°C for 3 seconds
 "non-Pb" units: 260°C for 3 seconds

1. Certain "with-Pb" package types are capable of 260°C for 3 seconds; please consult the factory for the latest information.

OPERATING RANGE

Range	Ambient Temp	V_{CC}
Industrial	-40°C to +85°C	3.3V

TABLE 2: DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Min.	Typ	Max.	Unit	Test Conditions
V_{CC}	Supply Voltage at pins 4, 14, 15, 16	2.7	3.3	3.6	V	
I_{CC}	Supply Current @ $P_{OUT} = 23$ dBm at $V_{CC} = 3.3$ V			400	mA	
I_{CQ}	V_{CC} quiescent current		130		mA	
I_{OFF}	Shut down current		<1.0		μA	
V_{REG}	Reference Voltage for recommended application		2.85		V	

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TABLE 3: AC ELECTRICAL CHARACTERISTICS FOR CONFIGURATION

Symbol	Parameter	Min	Typ	Max	Unit
F _{L-U}	Frequency range	4.9		5.8	GHz
Linearity	Output power with 4% EVM at 54 Mbps OFDM signal when operating at 3.3V V _{CC}		21		dBm
	Output power level with 802.11a mask compliance across 4.9-5.8 GHz	23			dBm
G	Linear gain across 4.9~5.8GHz	32			dB
G _{VAR}	Gain variation over band (4.9-5.8 MHz)			3	dB
	Gain variation over band (4.9-5.35 MHz)			1.5	dB
	Gain variation over band (5.7-5.8 MHz)			1	dB
	Gain variation over channel (20 MHz)		0.2		dB
Det	Power detector output voltage range	0.5		2.0	V
Det_ref	Power detector output reference	0.5	0.6		V
S	Power detector sensitivity		0.03		V/dB
Stability	Spurious output@ 25.5 dBm 54 Mbps OFDM signal when VSWR = 6:1 all angle			-60	dBc
Output VSWR Ruggedness	Survivable time@ 25.5 dBm (to 50Ω) 54 Mbps OFDM signal when VSWR = 10:1 all angle	10			second
2f, 3f, 4f, 5f	Harmonics at 22 dBm, without trapping capacitors		-40		dBc

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TYPICAL PERFORMANCE CHARACTERISTICS

TEST CONDITIONS: $V_{CC} = 3.3V$, $T_A = 25^\circ C$, $V_{REG1,2} = 2.85V$ UNLESS OTHERWISE NOTED

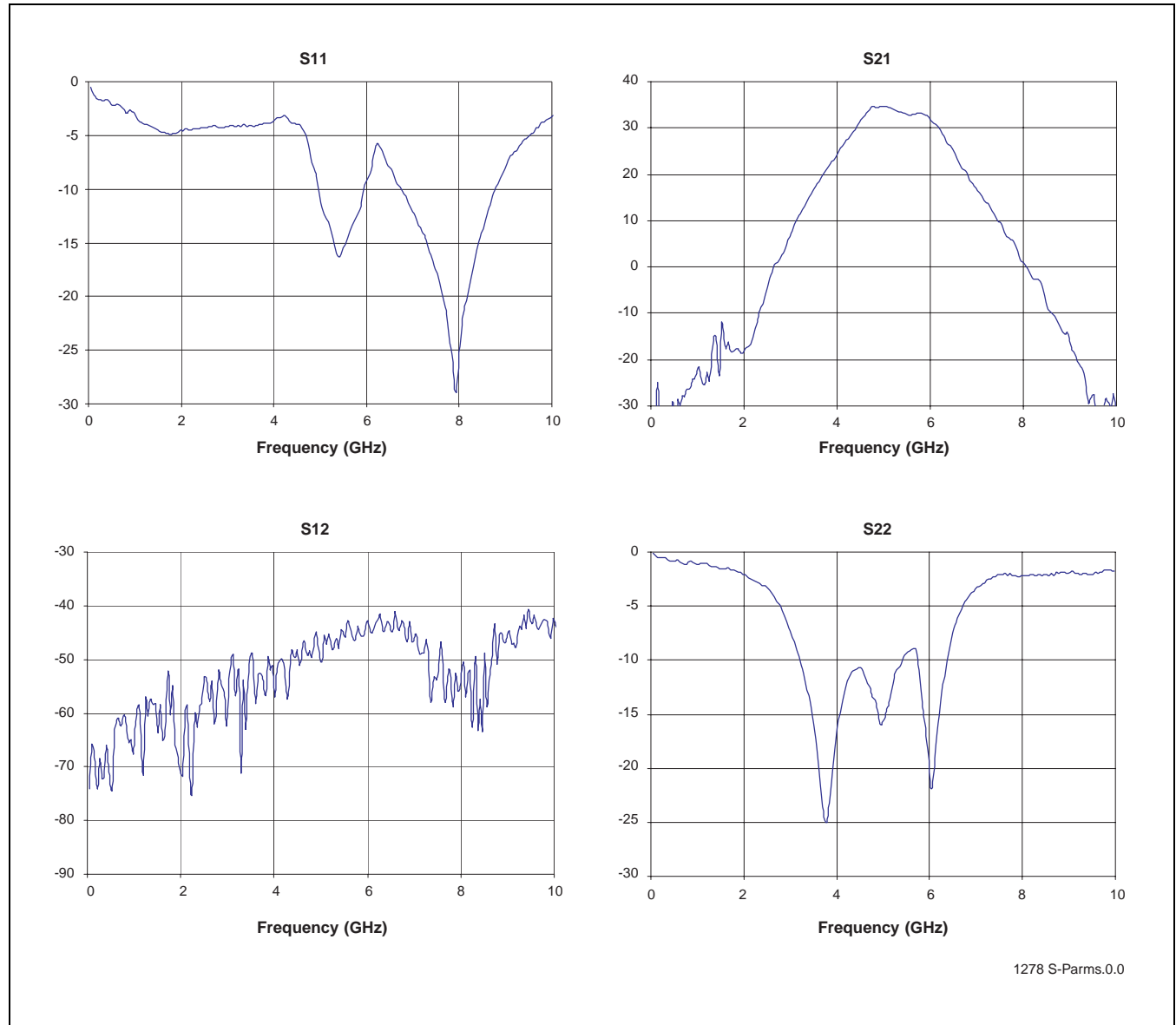


FIGURE 2: S-PARAMETERS



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TWO-TONE MEASUREMENTS

TEST CONDITIONS: $\Delta F = 1$ MHz

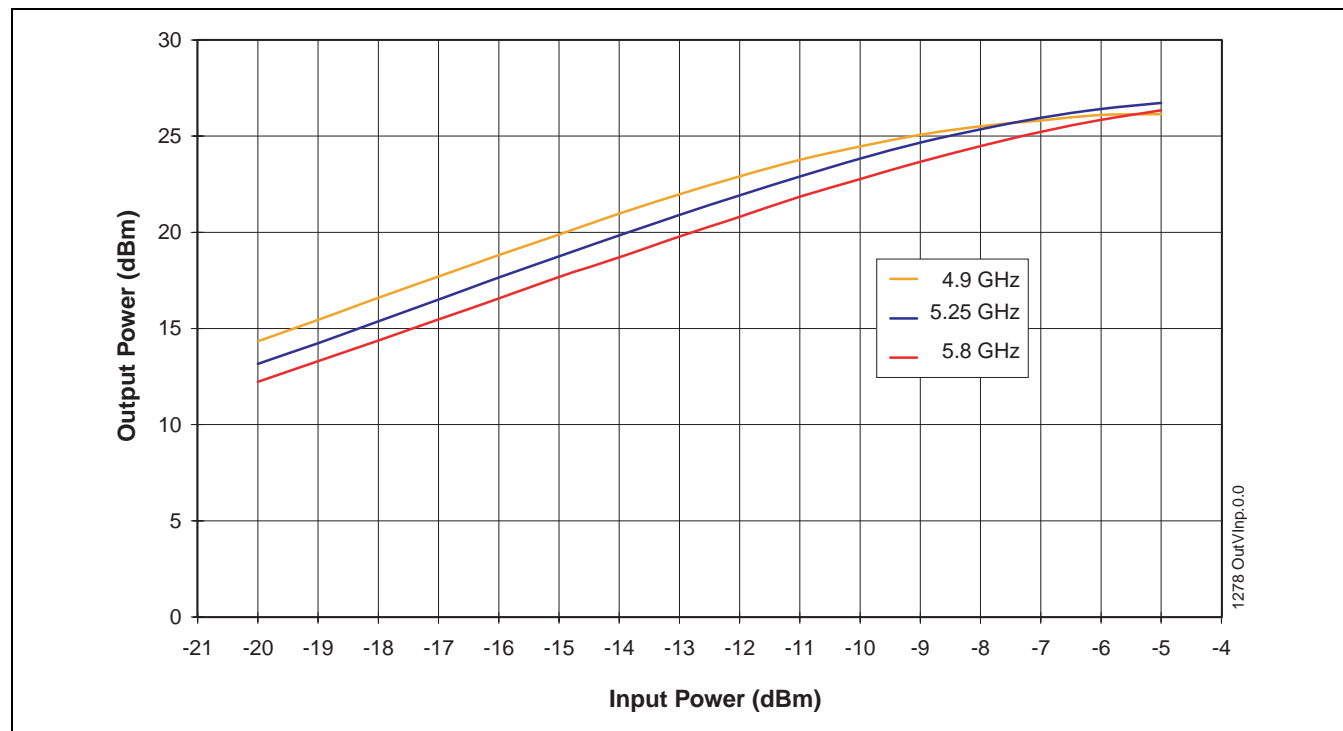


FIGURE 3: OUTPUT POWER VERSUS INPUT POWER

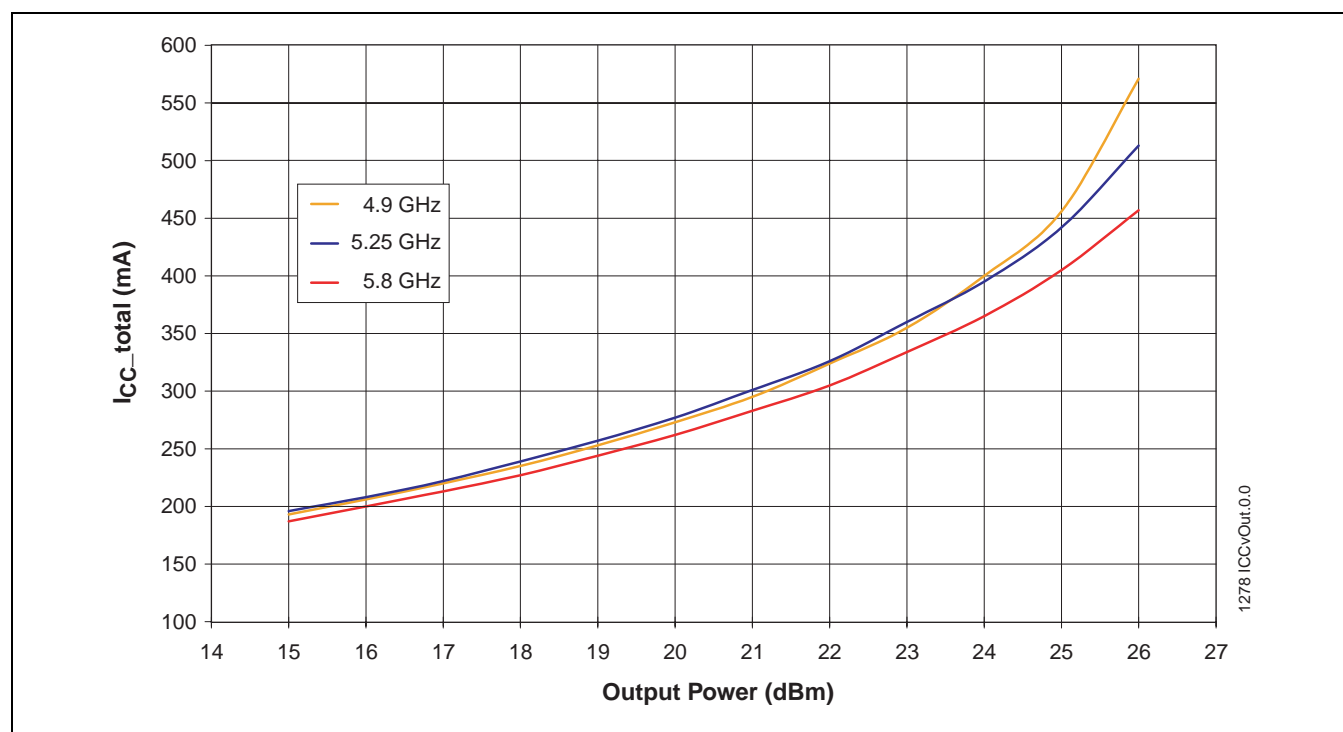


FIGURE 4: POWER SUPPLY CURRENT VERSUS OUTPUT POWER



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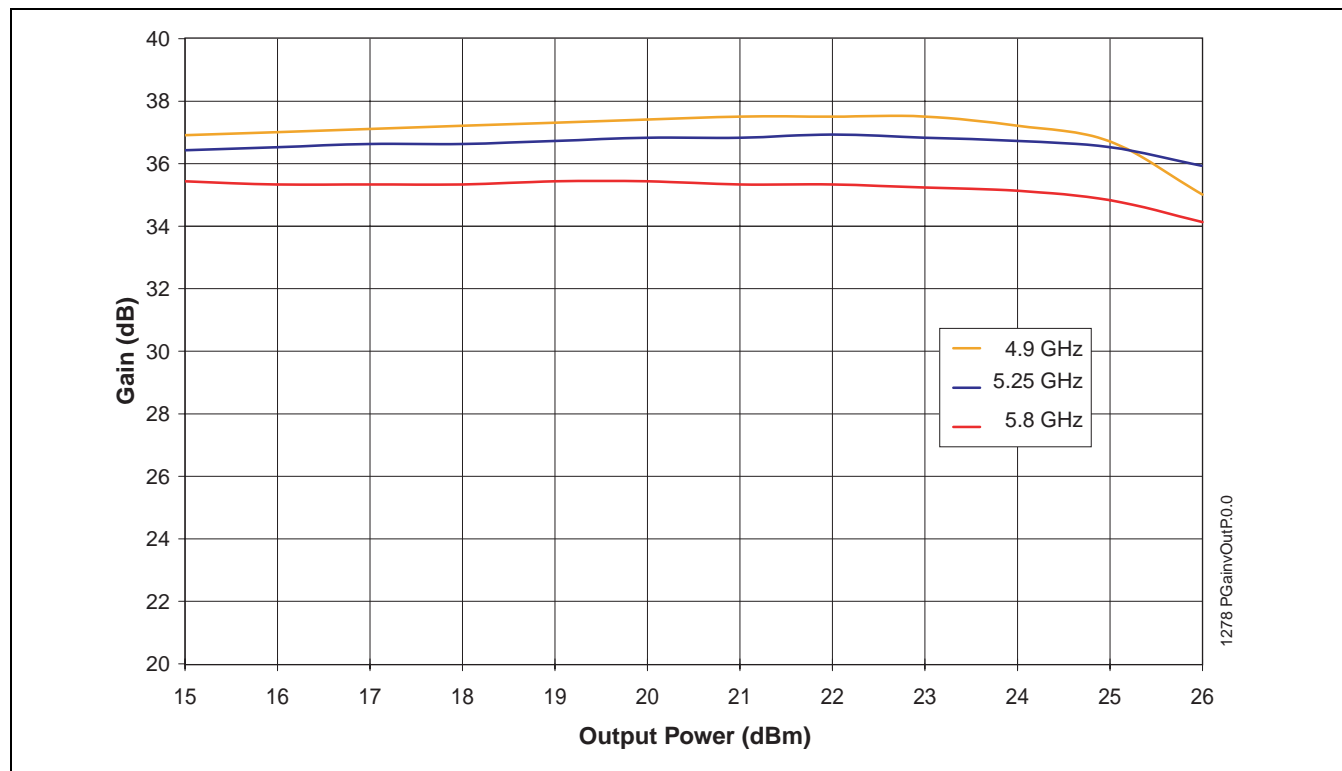


FIGURE 5: POWER GAIN VERSUS OUTPUT POWER

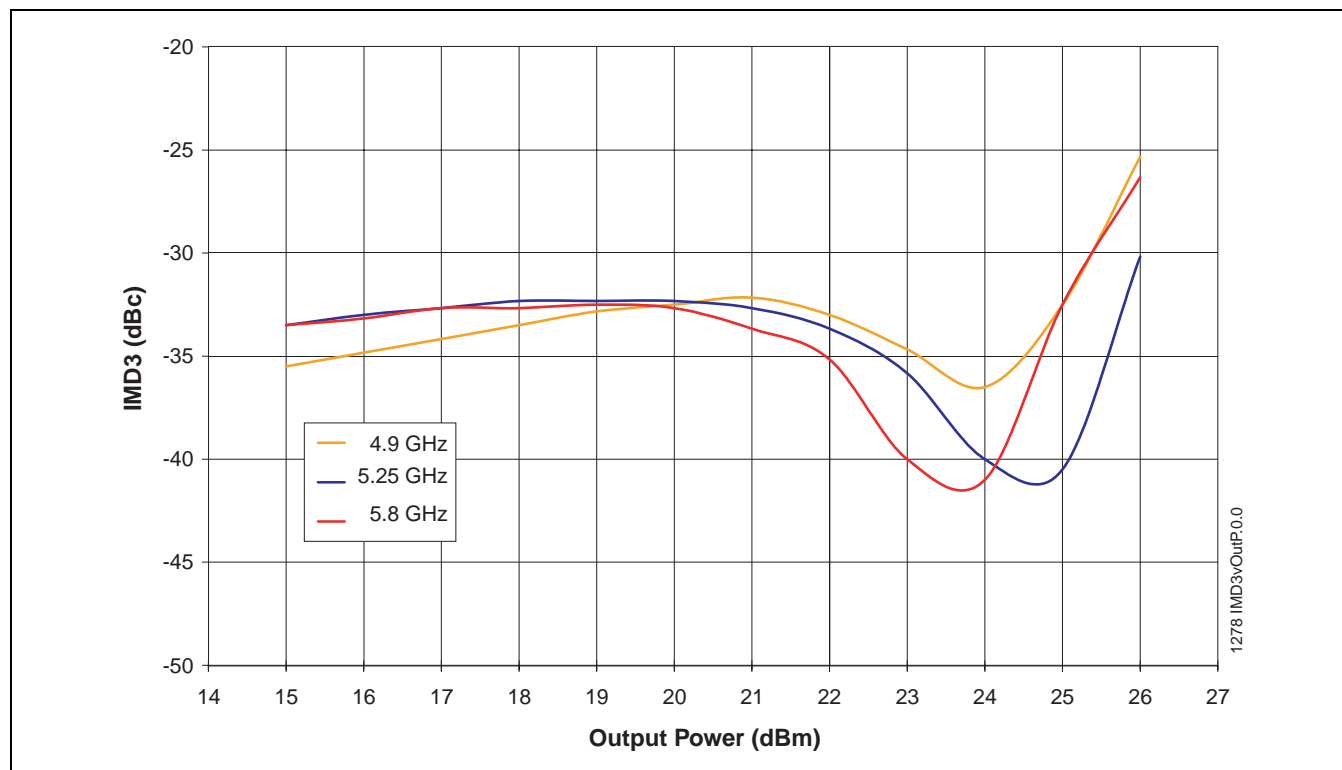


FIGURE 6: IMD3 VERSUS OUTPUT POWER

ACPR FOR 54 MBPS OFDM SIGNALS

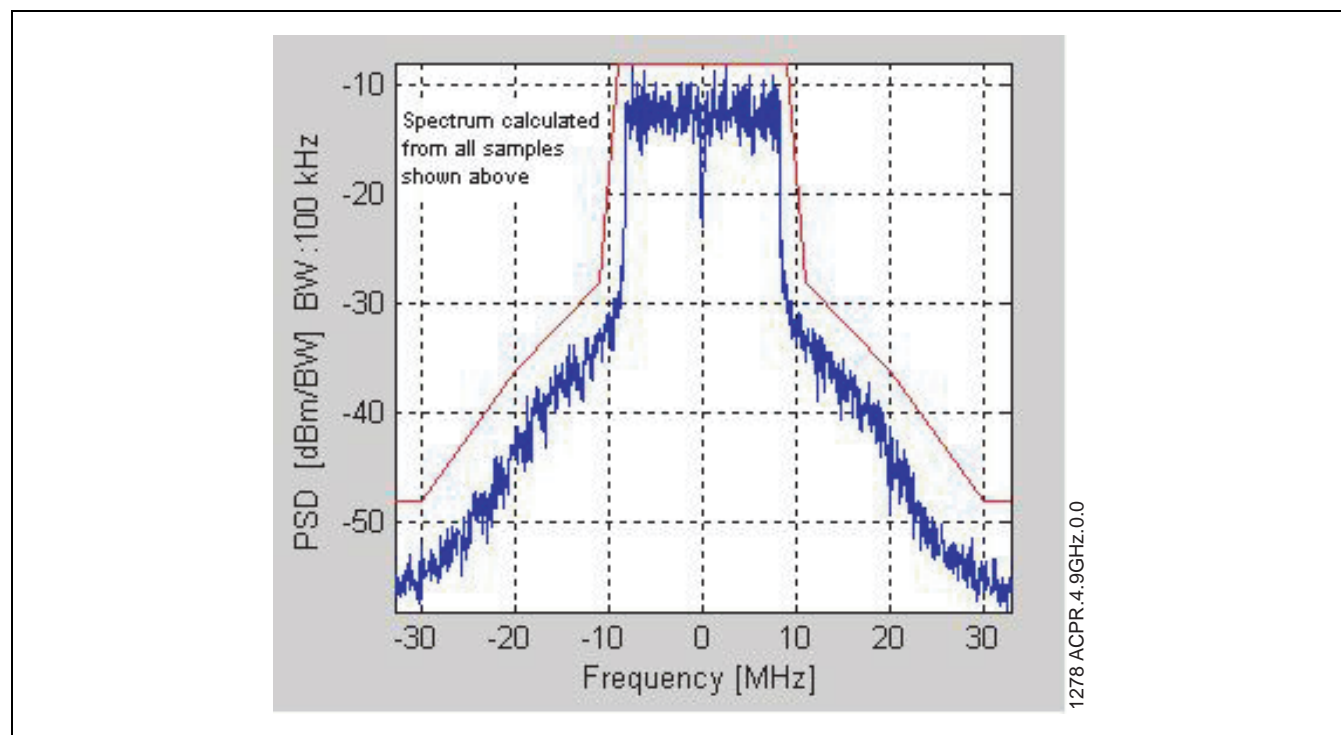


FIGURE 7: FREQUENCY = 4.9 GHz AT $P_{OUT} = 23.5$ DBM WITH $I_{CC} = 395$ MA

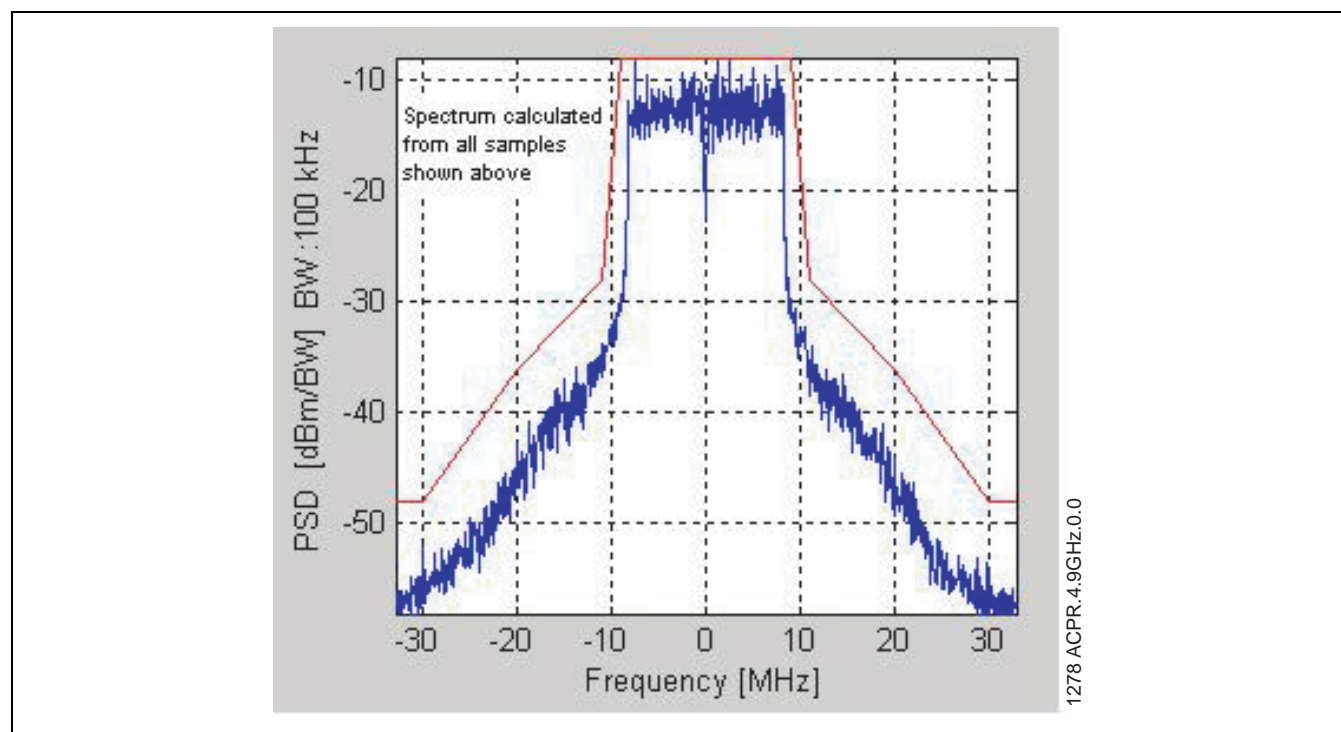


FIGURE 8: FREQUENCY = 5.18 GHz AT $P_{OUT} = 23.5$ DBM WITH $I_{CC} = 390$ MA



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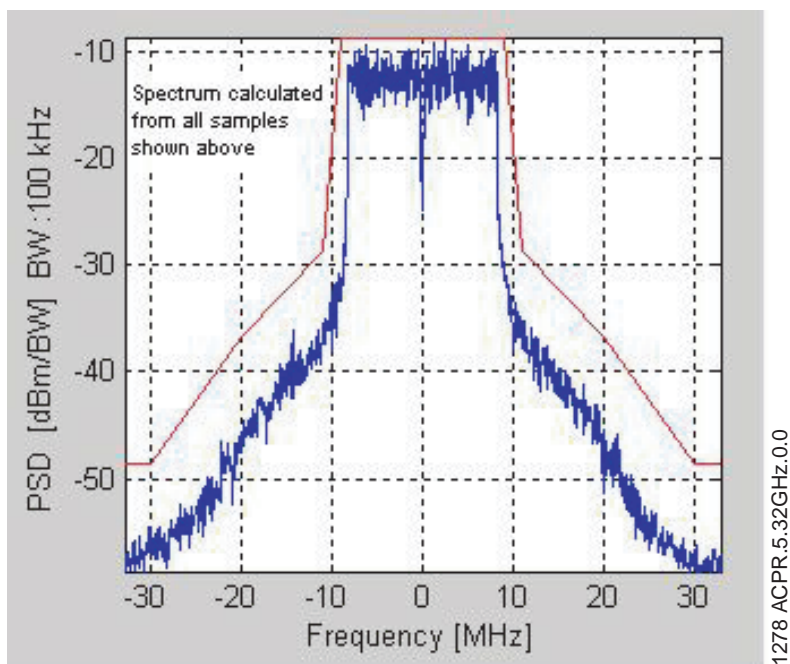


FIGURE 9: FREQUENCY = 5.32 GHz AT $P_{OUT} = 23.5$ dBM WITH $I_{CC} = 385$ mA

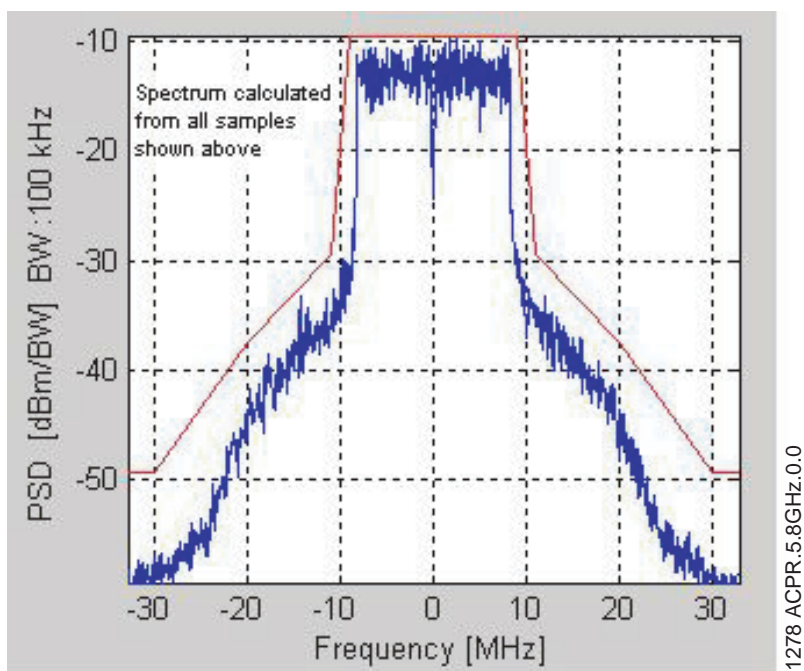


FIGURE 10: FREQUENCY = 5.8 GHz AT $P_{OUT} = 23.5$ dBM WITH $I_{CC} = 365$ mA



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EVM FOR 54 MBPS OPERATION

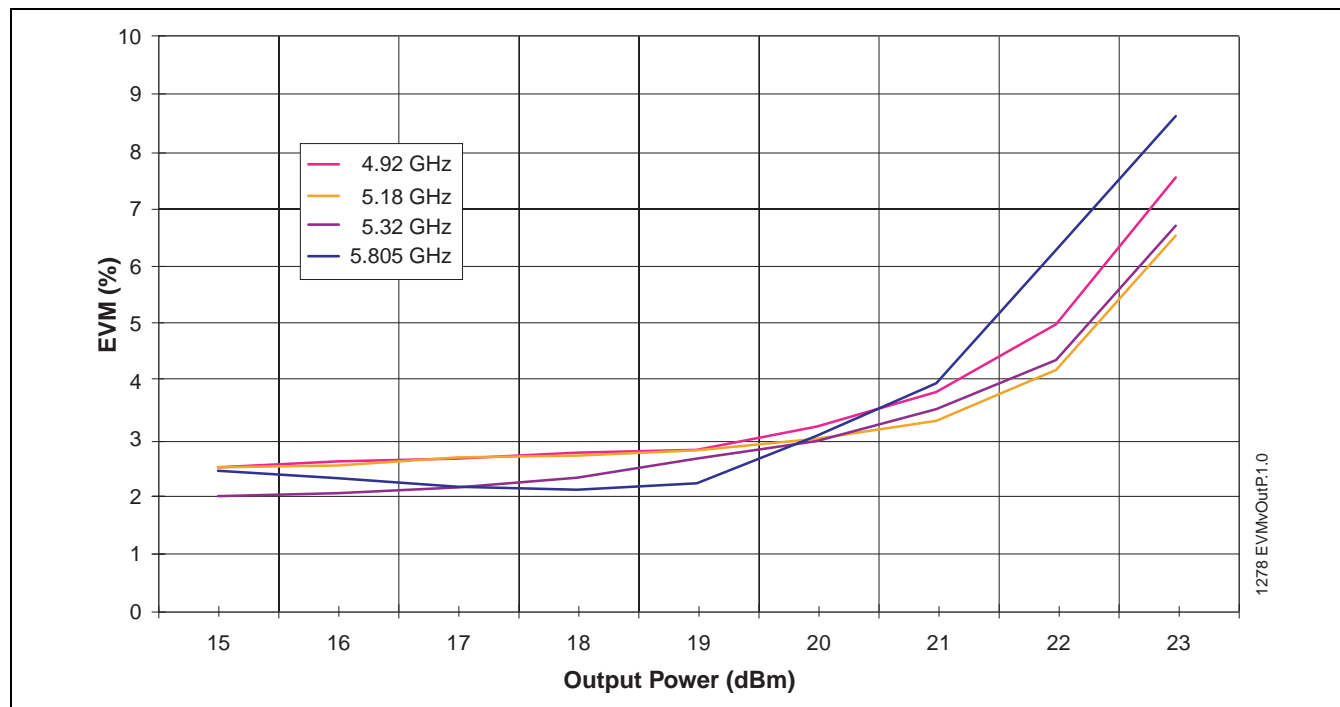


FIGURE 11: EVM VERSUS OUTPUT POWER

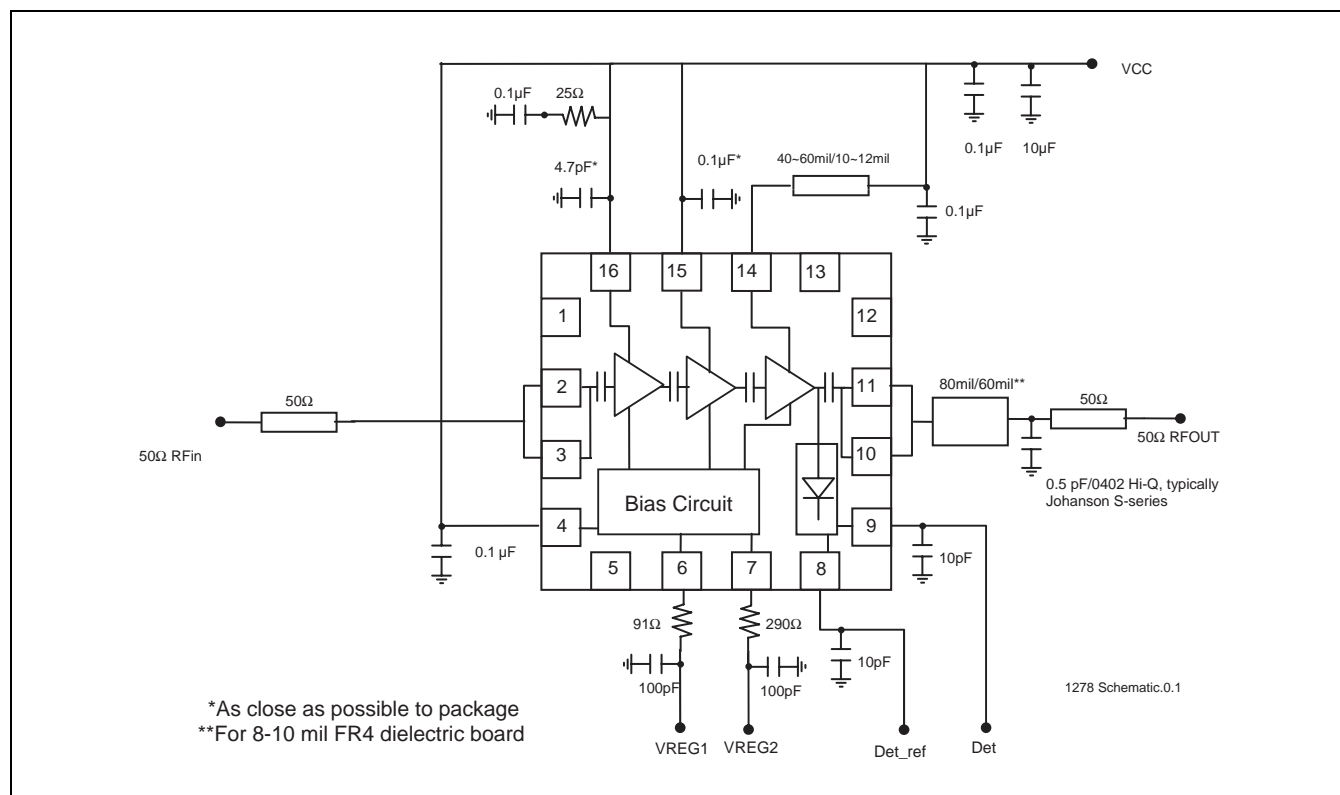


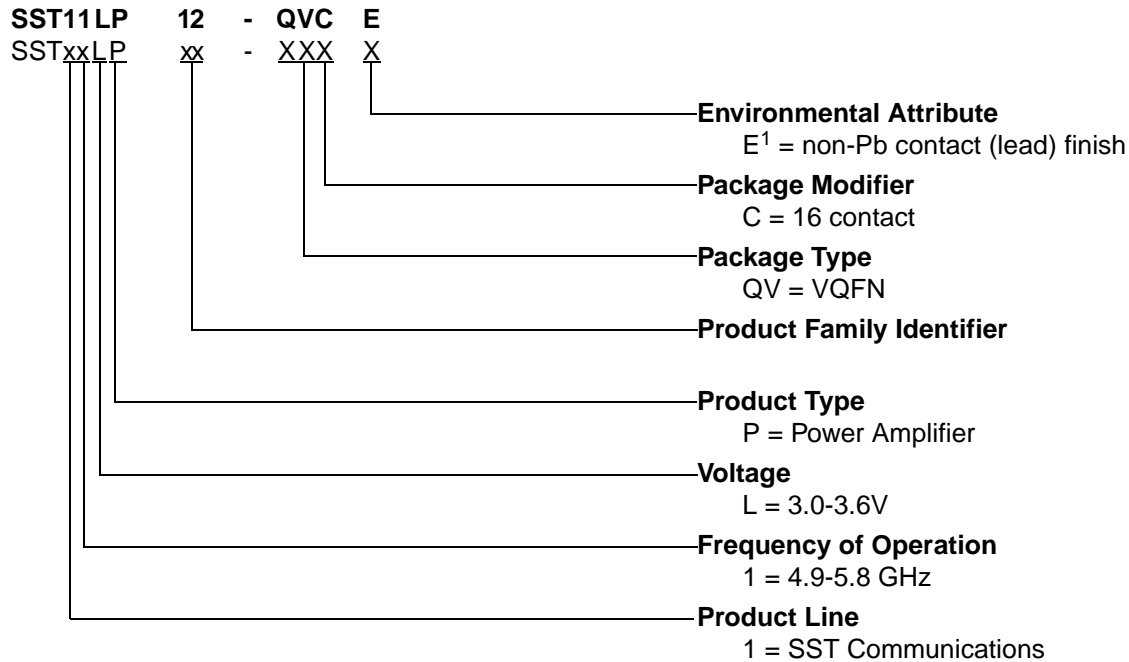
FIGURE 12: TYPICAL APPLICATION



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PRODUCT ORDERING INFORMATION



-
1. Environmental suffix "E" denotes non-Pb solder.
SST non-Pb solder devices are "RoHS Compliant".

Valid combinations for SST11LP12

SST11LP12-QVC
SST11LP12-QVCE

SST11LP12 Evaluation Kits

SST11LP12-QVC-K
SST11LP12-QVCE-K

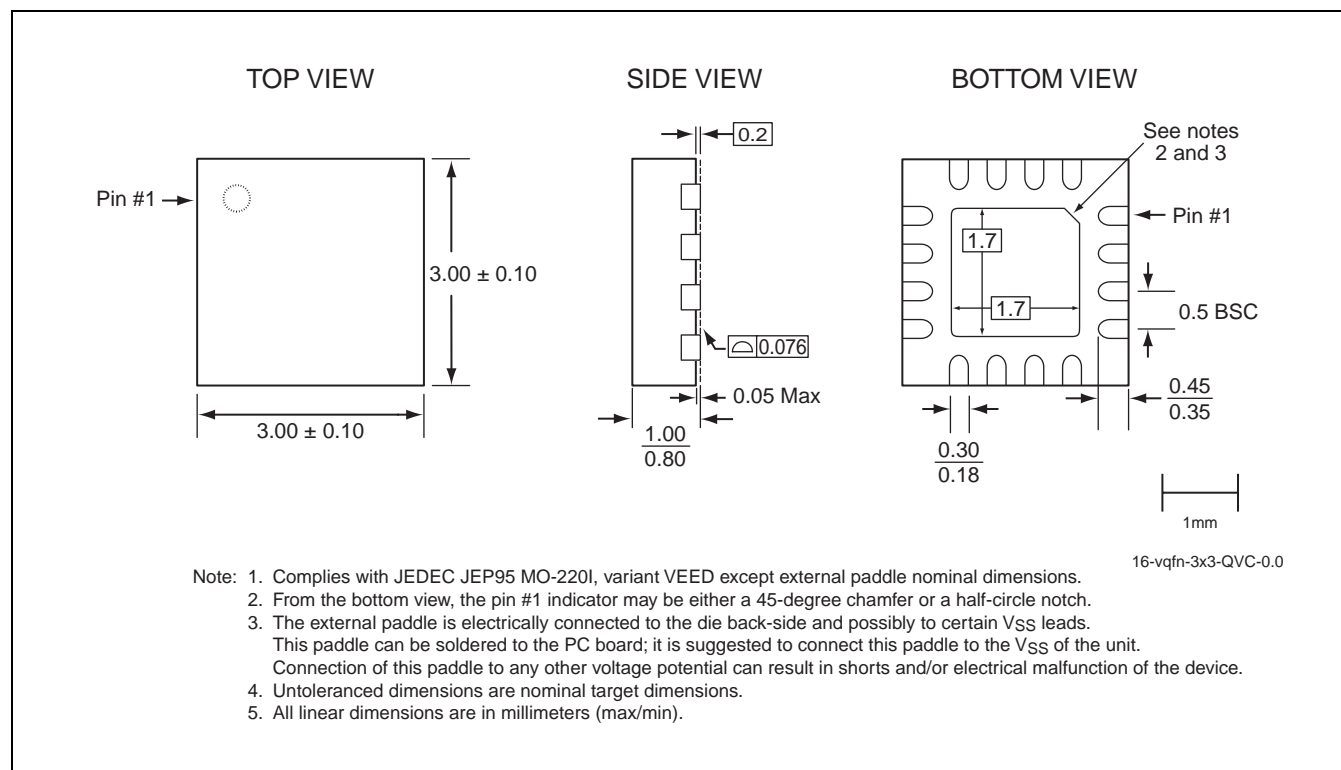
Note: Valid combinations are those products in mass production or will be in mass production. Consult your SST sales representative to confirm availability of valid combinations and to determine availability of new combinations.



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PACKAGING DIAGRAMS



16-CONTACT VERY-THIN QUAD FLAT NO-LEAD (VQFN)

SST PACKAGE CODE: QVC

TABLE 4: REVISION HISTORY

Revision	Description	Date
00	• S71278: SST conversion of data sheet GP1112	Jan 2005



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